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Interview. Was schedule a phone interview. The manager of the team took the initial phone interview. He started asking questions on Computer Architecture , super scalar , VLIW and ILP techniques.Cirrus Logic Design Verification Engineer Interview QuestionsDescription. Digital RTL design and Verilog interview questions is an initiative to help students/professionals who have basic knowledge of digital design and Verilog knowledge to quickly ramp up for an interview . The course is structured as 6 part series addressing the following interview question/concepts . Synchronizers interview questions part1 - MTBF , 2FF

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